

**DEPARTMENT OF ENGINEERING DESIGN  
INDIAN INSTITUTE OF TECHNOLOGY MADRAS**

**Ref. No: DEPT/ED/2015-16/09/001/Mechatronics Lab**

**Date: 8 September, 2015**

**Due Date: 30 September, 2015**

**ADDENDUM: ADDENDUM is marked with YELLOW HIGHLIGHT**

**Subject: INVITATION FOR QUOTATIONS FOR SUPPLY OF Digital Lock-In Amplifier.**

Dear Sir,

The following modifications/additional specifications are added in the Tender notification No. DEPT/ED/2015-16/09/001/Mechatronics Lab dated 8/9/2015. This should be read along with the other terms & conditions as already published in the website. Please refer to the YELLOW Highlight.

You may kindly provide a quotation for a digital lock-in amplifier with the following specifications.

**Digital Lock-In Amplifier specifications are as follows:**

- 1) Consisting of digital signal processing with dual phase lock-in amplifier
- 2) Voltage input: single-ended, or differential
- 3) Operating range: 1 mHz to 100 kHz or wider
- 4) Dynamic reserve: 100 dB or more
- 5) Sensitivity: minimum full-scale input voltage sensitivity 2 nV or less
- 6) Reference input: TTL or sine
- 7) Phase resolution: 0.01 degree or less
- 8) Built in direct digital synthesis source
- 9) Output: TTL or sine with digital display
- 10) Interfaces: IEEE-488.2 / RS-232 Standard / USB / High Speed Connection or Combination.
- 11) Power: 100-240 VAC, 50/60 Hz

For any clarification, please contact Dept. of Engineering Design, Telephone: +91-44-22574706 / 5630

Co-ordinator  
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