# Purchase of 12 node High Performance Computing Cluster

### Manufacture Eligibility:

- 1. Quoted OEM should have a minimum of 2 HPC Installation in Top500 list from <u>top500.org</u> (necessary proof of document to be enclosed with tender).
- 2. OEM should have minimum 3 Installation listed in Indian Supercomputer list (<u>http://topsc.cdacb.in/</u>)
- 3. Only OEMs should quote. If an OEM aligns with a business partner, then it should be one business partner per OEM (Authorization letter from OEM should be submitted in this regard)

### **Bidder Eligibility:**

 Bidder should have experience in deployment of HPC cluster in Research institutes or Centres of Govt. of India. The bidder alone or along with his OEM should have installed at least five (5) computing clusters in India or globally in last five years. Of these at least one HPC cluster should be more than ten (10) TFlops including only CPU. A document with the TFlop calculation should be provided. A minimum of 5 copies of purchase order for HPC (within last five Years only) should be submitted followed by successful Installation

Reports/Certificates along-with technical offer.

2. The Bidder should have registered support office with dedicated HPC Engineer capable of handling all HPC relevant issues. Necessary proof of employment should be submitted along with Certification if any.

# General terms:

- 1. The total cost must be specified including all the components of the whole system. Each bid should specify the details of the configuration and should specify the cost of each component.
- 2. The price should <u>also</u> be mentioned on per node (CPU/GPU) basis. Quantity of nodes may increase or decrease at the discretion of IIT Madras.
- 3. The quotation should meet all the <u>specified</u> technical specifications. A detailed compliance sheet in the format provided has to be submitted in accordance with the above specifications. Any deviations has to be highlighted and details must be mentioned.
- 4. Details of power and cooling requirements for the proposed cluster should be submitted along with the Technical bid. IITM will be providing the necessary Power and cooling infrastructure. Power socket details should be provided.
- 5. Complete installation and commission of HPC cluster system to be done by the bidder.

Master Node specifications: see specifications in the following pages

**Compute Nodes' specifications**: see specifications in the following pages

S. No.	Item	Technical Specification		
1	Total Number of nodes	<ol> <li>Master Node for login, job launch and other system administration activities</li> <li>compute nodes         <ol> <li>Storage node (100TB NAS)</li> </ol> </li> <li>At least 5 of the compute nodes to be CPU-GPU nodes.</li> </ol>		
2	Master node	<ul> <li>Processors - Intel Xeon Gold 5XXX series or above</li> <li>At least 192 GB DDR4 RAM for main memory in a balanced configuration per node with 2666 MHz or more.</li> <li>Two processors per node</li> <li>At least 1 TB of space overall per node with 7.2k or 10k rpm SAS Hard disk per node</li> <li>Provisions for future expansion of the cluster to double the capacity (24 nodes)</li> <li>Rack mountable with suitable mounting kit</li> <li>Next business day support</li> </ul>		
3	CPU-only compute nodes	<ul> <li>At Most 7 number of CPU only compute nodes</li> <li>Processors - Intel Xeon Gold 5XXX series or above</li> <li>Clock at least 32 instructions per core per cycle</li> <li>At least 18 cores per socket with 2.4GHz or more base processor frequency</li> <li>At least 192 GB DDR4 RAM for main memory in a balanced configuration per node with 2666 MHz or more.</li> <li>Two processors per node</li> <li>At least 2 TB space over all per node with 7.2K or 10K RPM SAS Hard disks</li> <li>One free PCI-e slot for future expansion</li> <li>Rack mountable with suitable mounting kit</li> <li>Redundant power supplies for all nodes</li> <li>Next business day support</li> </ul>		

4	CPU-GPU compute nodes	<ul> <li>NVIDIA A100 40/80GB cards (1 per GPU node).</li> <li>At least 5 of the compute nodes to be CPU-GPU nodes (2 preferable configuration options listed at the end of this document)</li> <li>If configuring 1 GPU card per node, then there should be a free PCIe slot in the node for further expansion later on.</li> <li>At least 32 instructions per core per cycle</li> <li>At least 192 GB main memory in a balanced configuration per node with 2666 MHz or more</li> <li>At least 18 cores per socket with 2.4GHz or more base processor frequency</li> <li>At least 2 TB space over all per node with 7.2K or 10K RPM SAS Hard disks</li> <li>Redundant power supplies</li> <li>Rack mountable with suitable mounting kit</li> <li>Vendor should quote for PCI-e and NVLink separately, if applicable</li> <li>Next business day support</li> </ul>		
5	Total compute power	<ul> <li>The overall system must support expansion to at least 24 compute nodes (in the same ratio of GPU-CPU nodes).</li> <li>For additional expansion in future, the vendor must offer at the bid price or below</li> <li>Bidder must also include the total computing performance in TFlops for the overall system on SPEC benchmark and total GPU-CPU performance must be at least 10T Flops.</li> </ul>		
6	Storage Node	<ul> <li>At least 100TB NAS in total for usable file system provided in the storage node</li> <li>At least RAID 5 across the total storage.</li> <li>It would be split as 60% of scratch area and 40% of home storage both available in all nodes</li> <li>Backup up of 40% of home with a NAS Storage of 10TB; with Necessary auto backup/restore control</li> <li>Processor with at least 16 cores, at least 96GB RAM in a balanced configuration</li> </ul>		
7	Interconnect	<ul> <li>Infiniband switch with at least 100 Gbps bandwidth that can support future extension to double the size of the present cluster and adequate number of IB adapters for the cluster quoted.</li> <li>All switches should have redundant power supplies.</li> <li>At least 1 Gbps network card</li> </ul>		

8	Power	As required for the solution above; the vendor is expected to visit the site to understand the existing infrastructure.		
9	Rack mounting kit and enclosure	As required for the solution above and should support expansion to at least 24 nodes. The rack should be 42U or smaller.		
10	Operating system/ Cluster Manage ment	<ul> <li>Licensed OS (64-bit Linux or Unix variant) and licensed cluster management/monitoring software should be fully supported (by the OEM), with updates during the warranty period. Cluster management software should be licensed without the limits on the number of nodes.</li> </ul>		
11	Compilers , libraries and tools	<ul> <li>Support for the complete software suite including all the software currently supported on the Aqua cluster at IITM (OpenFOAM, Abaqus, Comsol, Mathematica, Gaussian, Nastran, Dytran, Marc, Ansys/fluent, Namd, Lammps, Gromacs, Amber, Accelerys, Matlab.) along with various compilers (such as GNU GCC collection, intel, java compilers). Also, GPU Computing and AI related software/SDK such as - NVIDIA HPC SDK v20.x, CUDA V9.x, 10.x, 11.x, Tensorflow/Pytorch/FastAi/XGBoost, CUDNN v7.x. Modules environment package for managing software environments.</li> </ul>		
12	System administration	<ul> <li>All HPC components should be administered from a single management console/master node.</li> </ul>		
13	Software reliability	<ul> <li>Software failover for critical system services including system database, system logger and batch file systems, parallel file systems;</li> </ul>		
14	Hardware reliability	<ul> <li>Redundant paths for all system RAID</li> <li>Redundant power supplies and voltage regulator modules</li> </ul>		
15	Job management	<ul> <li>Licensed and commercial-grade or open-source HPC workload and resource management with support for the entire lifetime of the cluster.</li> <li>Accounting capability to summarize the total CPU usage of a user in a specific period.</li> </ul>		
16	Safety	International standard safety level		

17	Onsite warranty	• Entire HPC solution including each software, firmware and hardware component should have at least a 5 years warranty from the HPC solution provider, from the date of acceptance.	
18	Installation & Support	<ul> <li>The vendor should install and test following as well as any other required libraries/packages for running cluster.         <ul> <li>Operating system</li> <li>Cluster management tools (Open Source)</li> <li>Job scheduler installation</li> <li>C, C++, FORTRAN compilers</li> <li>Parallelization softwares like MPI, OpenMP and Pthread</li> <li>Web based management software for monitoring, managing and configuring servers.</li> </ul> </li> <li>The hardware of the cluster must be factory integrated, installation of the hardware, OS, software components must be done by trained engineers.</li> <li>During the 5 year period of warranty, bidder must provide complete management support for the cluster i.e., comprehensive fault management and performance management including software required to perform these tasks.</li> <li>The specifics of the 24 x 7 support in terms of response and resolution time for various types of problems for the HPC cluster must be mentioned separately.</li> <li>Committed performance benchmark numbers to be demonstrated post installation as an acceptance criteria.</li> </ul>	

#### Options that the vendors should quote for:

- 1. All vendors are required to bid for all the options below failing which the bids submitted will be totally rejected.
- 2. IITM reserves all rights to choose any one of the options from the specifications given above.
- 3. The orders will be placed only for one option and the decision on the option to be picked up will be made during the final comparison of financial bids.

Option	CPU Specs	#CPU Nodes	#GPU Nodes	GPU and #GPU cards per GPU
A	At least 18 cores per socket	7	5	NVIDIA <b>A100</b> 40/80GB ( <b>1</b> per node)
С		6	6	NVIDIA <b>A100</b> 40/80GB ( <b>1</b> per node)

Nandan Sudarsanam,

सह प्राध्यापक / Associate Professor प्रवंधन अध्ययन विभाग Department of Management Studies आई.आई.टी.पट्रास / IIT Madras चेन्ने / Chennai - 600 036 भारत / India

Dr A.N. Rajagopalan

Dr R. Raghunathan