Technical specification of Data Acquisition System (DAQ) with chassis and accessories

A brief note on equipment:

DAQ is used to obtain data generated during metal cutting operations in a customized manner and then store it in a digital format in a computer for further analysis

S.NO	PARTICULARS	REQUIREMENT	COMPLIED/NOT COMPLIED	CATALOGUE PAGE NO
1.	Time synchronised	d DAQ chassis(4 slot USB) and its accessories		
	The chassis module can connect to various sensors which measures temperature, sound, vibration, etc. It enables acquiring of data generated during real time phenomenon by converting them into current and voltages.			
	Analog Input	 Input FIFO size: 120 and above samples per slot. Maximum sample rate: Determined by the C Series I/O module or modules Timing accuracy: 45 and above ppm of sample rate Timing resolution: 14 ns and below Number of channels supported: Determined by the C Series I/O module or modules 		
	Analog output	 Hardware-timed task: On board regeneration: 13 and above Non-regeneration: Determined by the I/O module or modules Non-hardware-timed task: Determined by the I/O module or modules or modules 		
	Maximum update rate	 On board regeneration:1.2 and above MS/s (multi-channel, aggregate) Non-regeneration: Determined by the I/O module or modules Timing accuracy: 45 and above ppm of sample rate Timing resolution: 15 ns and below 		
	Output FIFO size	 □ On board regeneration: 8,000 and above samples to be shared among channels used □ Non-regeneration: 110 samples and above per slot □ Input voltage range: 9 to 30 V □ Maximum required input power:15W □ Operating temperature: -20 °C to 		

2	Voltage input module with required accessories		COMPLIED/NOT COMPLIED	CATALOGUE PAGE NO
	It enables working with voltage measurement applications and offers over			molino
	No. of Channels	□ 3 and above		
-	ADC Resolution	\Box 14 bits and above		
	Type of ADC	□ 14 bits and above □ Successive approximation register (SAR)		
	Input Voltage ranges	 ☐ Minimum: ±10.5V ☐ Typical: ±10.6V ☐ Maximum: ±10.7V 		
	Overvoltage Protection	$\Box \pm 28$ V and above		
	Maximum Sampling Rate	\Box 900 KS/s and above		
	Stability	 Gain Drift: 7ppm/ °C and below Offset Drift: 31μV/ °C and below CMRR(fin =60Hz): 95 dB and above -3 dB bandwidth: >1MHz Input Impedance: >0.9 GΩ Noise:0.8 LSBrms and below Total Harmonic Distortion: -90 dB and above Crosstalk(20Vpp at 1KHz): -110 dB and above 		
	Power Requirements	 Power Consumption from chassis: Active Mode: 1 W max. Sleep Mode: 5mW max. 		
	Thermal dissipation (at 70 °C)	 Active Mode: 1.3 W max. Sleep Mode: 430 mW max. 		
	Operating temperature	□ -40℃ to 70℃		

Other conditions	\Box The Bidder can be OEM/	
	authorized distributor of the DAQ	
	module and chassis manufacturing	
	company.	
	\Box It is mandatory for the	
	Bidder(Authorized Distributor) to	
	provide the Tender specific	
	authorization letter from the	
	manufacturer(OEM)	
	□ The DAQ module should support	
	compatible programming software	
	like Labview, and Mathworks Matlab	
	etc	
	\Box The quoted price should be	
	inclusive of transportation,	
	installation and training	
	\Box The vendor has to give	
	demonstration of the successful	
	working of the item being	
	procured as per aforesaid	
	specifications	
	□ Delivery time: preferably within 4	
	months from the issuance of the	
	purchase order.	
	\Box Warranty: 1 year from the date	
	of installation	
	or mountation	

(Note: It is mandatory for the bidders to provide the compliance statement in tabular column format along with catalogue page number (comply/not comply) for the Above points with document proof as required. Failing which bidders will be technically disqualified)