HPC COMPUTER NODES:

Requirements: The development of A high-performance computing cluster is proposed at IIT Madras. The proposed cluster will be a collection of several state-of-the-art servers connected through an infiniband interconnect. In the following, detailed technical specifications required for developing such a facility are listed:

HPC Head Node: Procurement of 1 head node is proposed with the specifications listed as follows:

S. No.	Parameters	Quantity	Values/Notes
1.	Processor	2	Intel Xeon Gold 5218R 2.10GHz 20 Core
			27.5MB
2.	Memory	4	32GB DDR4 ECC-Registered 2933MHz
			Memory
3.	Power	1	2U Chassis with 1000W Redundant Power
	Supply		Supplies Titanium Level 96% efficiency
4.	Socket	1	Dual Coalest Doord summents Intal Voor Cleviales
4.		1	Dual Socket Board supports Intel Xeon Skylake
	Board		processor
5.	Storage	7	Enterprise Class 10TB SATA HDD
6.	Storage	1	480 GB Enterprise Class SSD
7.	Raid	1	8-port Raid Card
	Controller		
8.	Support and	1	3 years on-site warranty
	Maintenance		
9.	Interconnect	1	Infiniband EDR 100 GB 1-port adapter with
			accessories

HPC Compute Nodes: Procurement of 8 compute nodes is proposed with each compute node having the specifications listed as follows:

S. No.	Parameters	Quantity	Values/Notes
10.	Processor	2	Intel Xeon Gold 5218R 2.10GHz 20 Core
			27.5MB
11.	Memory	4	32GB DDR4 ECC-Registered 2933MHz
			Memory
12.	Power	1	1U Chassis with 750W Redundant Power
	Supply		Supplies Platinum Level high efficiency
13.	Socket	1	Dual Socket Board supports Intel Xeon Skylake
	Board		processor
14.	Storage	1	Enterprise Class 5 TB SATA HDD

15.	Support and	1	3 years on-site warranty
	Maintenance		
16.	Interconnect	1	Infiniband EDR 100 GB 1-port adapter with
			accessories

HPC Compute Nodes: Procurement of 4 compute nodes is proposed with each compute node having the specifications listed as follows:

S. No.	Parameters	Quantity	Values/Notes
10.	Processor	2	Intel Xeon Gold 5218R 2.10GHz 20 Core
			27.5MB
11.	Memory	4	32GB DDR4 ECC-Registered 2933MHz
			Memory
12.	Power	1	2U Chassis with 1000W Redundant Power
	Supply		Supplies Titanium Level 96% efficiency
13.	Socket	1	Dual Socket Board supports Intel Xeon Skylake
	Board		processor
14.	Storage	1	Enterprise Class 5 TB SATA HDD
			Provision for 8 slot storage (expandable to 8x
			TB)
15.	Support and	1	3 years on-site warranty
	Maintenance		
16.	Interconnect	1	Infiniband EDR 100 GB 1-port adapter with
			accessories

Interconnect: Infiniband switch

17.	Infiniband	1	Refurbished 24 port 100 GB/s QDR Infiniband
	switch		switch
			unmanaged with 3 years onsite comprehensive
			warranty

Other specifications:

18.	Supplier	1. OEM (Manufacturer) should have at
	Capability	least 5 HPC installations in latest listing of Top 500 supercomputer list.
		2. The bidder (systems integrator) should
		have installed and implemented 5 HPC
		clusters within the last 3 years with a
		minimum size of 5 nodes each along
		with infiniband. The PO's and
		installation reports of those installations
		should be furnished.

- 3. The bidder (systems integrator) should submit the authorization letter from the OEM's. OEM should authorize only one bidder & the bidder also bid only one OEM product.
- 4. The bidder (system integrator) should have technical expertise in implementing cluster, which includes installation configuration, maintenance and customization as per requirement. The details of the engineers should be furnished together with the technical bid.
- The Bidder/System Integrator should have minimum 10 years of Experience in HPC Business and Proof of Order has to be submitted
- The Bidder should have Support office in Chennai and HPC Engineer Details should be Furnished
- Bidder should have done at least 1 installation of 50TF of Sustained HPC performance
- Bidder should propose the Technical Presentation post the bid submission to prove their technical capability.
- The firm must provide a self-declaration that there is no complaint/vigilance inquiry against them in any Govt./Department /PSU and they have not been black listed by any Govt Department/PSU
- 10. The bidder should integrate the following list of applications (but not limited to) with the system
 - 1. Open source compilers (gcc, g++, gfortran, python)
 - 2. Intel Compilers for C++ and Fortran
 - 3. Open source software:
 ParaView, VIsIt, GVim, EMacs,
 Octave, Scilab, MPI, OpenMP,
 Gnuplot, Gaussian
- 11. Commercial Software (the required licenses will be provided by IITM):
 Ansys, Abaqus, MATLAB, COMSOL,
 Mathematica