

## CORRIGENDUM – 2

### CHANGES IN TECHNICAL SPECIFICATION:

S.No	Technical Specification	Revised Technical Specification
1.	<p data-bbox="302 544 607 584"><u>Annexure IV, p 6</u></p> <p data-bbox="302 639 490 679"><u>The clause</u></p> <p data-bbox="302 735 1084 826"><b>A.</b> 7 CPU Node + 3 GPU Node : NVIDIA V100 32GB (2 per node)</p> <p data-bbox="302 882 1084 973"><b>C.</b> 6 CPU Node + 4 GPU Node : NVIDIA V100 32GB (2 per node)</p>	<p data-bbox="1106 544 1426 584"><u>Annexure IV, p 6</u></p> <p data-bbox="1106 639 1382 679"><u>Should read as</u></p> <p data-bbox="1106 735 1850 826"><b>A.</b> 7 CPU Node + 3 GPU Node : NVIDIA V100/TESLA A100 32GB (2 per node)</p> <p data-bbox="1106 882 1850 973"><b>C.</b> 6 CPU Node + 4 GPU Node : NVIDIA V100/TESLA A100 32GB (2 per node)</p>