## <u>CORRIGENDUM – 3</u>

**Tender Reference Number: MGT/2021/IOECOE2/009/NODEHPCC** 

**Tender name: 12 node High Performance Computing Cluster** 

## **CHANGES IN TECHNICAL SPECIFICATION:**

S.No	Technical Specification	Amendment
1.	Manufacture Eligibility:1. Quoted OEM should have a minimum of 2 HPCInstallation inTop500 list from top500.org (necessary proof of document to be enclosed with tender).2. OEM should have minimum 3 Installation listed in Indian Supercomputer list (http://topsc.cdacb.in/)	This clause has been removed.

## **RESPONSE TO THE QUERIES:**

S.No.as per Tender Documents	Item as per Tender Documents	Technical Specification as per Tender Documents	Bidders Query	Reply to Query
2	Master node	Processors - Intel Xeon Gold 5XXX series or above	Kindly give the more details for processor i.e No of cores per socket for each processor, Details of GHz speed and base clock frequency	This is only the master node. The clock frequency could be $>= 2.5$ GHz and the total number of cores across all the sockets could be $>= 16$
6	Storage Node	At least 100TB NAS in total for usable file system provided in the storage node - At least RAID 5 across the total storage.	<ol> <li>What is the meaning of scratch area and home storage?</li> <li>How many nodes are required?</li> <li>SATA HDD or NLSAS</li> </ol>	<ol> <li>Home area is for home directories of users. Scratch area is for simulation data.</li> <li>Single node, with</li> </ol>
		<ul> <li>It would be split as 60% of scratch area and 40% of home storage both available in all nodes</li> <li>Processor with at least 16 cores, at least 96GB RAM in a balanced configuration</li> </ul>	HDD is required?	<ul> <li>storage accessible to all the master + compute nodes.</li> <li>3. Either of these are fine.</li> </ul>