


Basic Details

Organisation Chain	Indian Institute of Technology Madras Deputy Registrar (Industrial Consultancy and Sponsored Research)		
Tender Reference Number	EE/SAUR/2021/2MM TSMC65GPCMOS		
Tender ID	2021_IIT-M_605156_1		
Tender Type	Limited.	Form of contract	Supply
Tender Category	Goods	No. of Covers	2
Payment Mode	Not Applicable	Is Multi Currency Allowed For BOQ	No
Is Multi Currency Allowed For Fee	No		

Cover Details, No. Of Covers - 2

Cover No	Cover	Document Type	Description
1	Fee/PreQual/Technical	.pdf	Technical bid
2	Finance	.xls	Financial bid

Tender Fee Details, [Total Fee in ₹ * - 0.00]

Tender Fee in ₹	0.00	Fee Payable To	NA	Fee Payable At	NA
Tender Fee Exemption Allowed	NA				

EMD Fee Details

EMD Amount in ₹	0.00	EMD Exemption Allowed	NA
EMD Fee Type	NA	EMD Percentage	NA
EMD Payable To	NA	EMD Payable At	NA

Work /Item(s)

Title	Two Quantities of 2mm x 1.04mm IC in TSMC 65 GP CMOS process				
Work Description	Two Quantities of 2mm x 1.04mm IC in TSMC 65 GP CMOS process				
Pre Qualification Details	Please refer Tender documents.				
Tender Value in ₹		Product Category	Miscellaneous Goods	Sub category	NA
Contract Type	Tender	Bid Validity(Days)	90	Period Of Work(Days)	10
Location	Department of Electrical Engineering	Pincode	600036	Pre Bid Meeting Place	NA
Pre Bid Meeting Address	NA	Pre Bid Meeting Date	NA	Bid Opening Place	Department of Electrical Engineering

Critical Dates

Publish Date	19-May-2021 04:30 PM	Bid Opening Date	28-May-2021 04:00 PM
Document Download / Sale Start Date	19-May-2021 04:40 PM	Document Download / Sale End Date	28-May-2021 03:00 PM
Clarification Start Date	NA	Clarification End Date	NA
Bid Submission Start Date	20-May-2021 09:00 AM	Bid Submission End Date	28-May-2021 03:00 PM

Tender Documents

NIT Document	S.No	Document Name	Description	Document Size (in KB)
	1	Tendernotice_1.pdf	As per the tender document	284.6

Work Item Documents	S.No	Document Type	Document Name	Description	Document Size (in KB)
	1	Additional Documents	ANNEXURE - III.pdf	ANNEXURE - III	3.39
2	Tender Documents	LIMITED TENDER - 2MM TSMC 65 GP CMOS.pdf	LIMITED TENDER	284.6	

Tender Inviting Authority

Name	Dr. Saurabh Saxena
Address	Dr. Saurabh Saxena, Department of Electrical Engineering IIT Madras, Sardar Patel Road, Chennai - 600 036

Tender Creator Details

Created By	Vijay Shankar B
Designation	Assistant Registrar
Created Date	19-May-2021 04:08 PM