

**Basic Details**

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|--|--|--|--------|
| Organisation Chain | Indian Institute of Technology Madras Deputy Registrar (Industrial Consultancy and Sponsored Research) | | |
| Tender Reference Number | EE/2021/IOECOE/006/MEMORYARRAY | | |
| Tender ID | 2021_IIT-M_590374_1 | | |
| Tender Type | Open Tender | Form of contract | Supply |
| Tender Category | Goods | No. of Covers | 2 |
| Payment Mode | Not Applicable | Is Multi Currency Allowed For BOQ | No |
| Is Multi Currency Allowed For Fee | No | | |

Cover Details, No. Of Covers - 2

| Cover No | Cover | Document Type | Description |
|----------|-----------------------|---------------|---------------|
| 1 | Fee/PreQual/Technical | .pdf | Technical bid |
| 2 | Finance | .pdf | Financial bid |

Tender Fee Details, [Total Fee in ₹ * - 0.00]

| | | | | | |
|-------------------------------------|------|-----------------------|----|-----------------------|----|
| Tender Fee in ₹ | 0.00 | Fee Payable To | NA | Fee Payable At | NA |
| Tender Fee Exemption Allowed | NA | | | | |

EMD Fee Details

| | | | |
|------------------------|------|------------------------------|----|
| EMD Amount in ₹ | 0.00 | EMD Exemption Allowed | NA |
| EMD Fee Type | NA | EMD Percentage | NA |
| EMD Payable To | NA | EMD Payable At | NA |

Work /Item(s)

| | | | | | |
|----------------------------------|---------------------------------------|-----------------------------|---------------------|------------------------------|--------------------------------------|
| Title | MEMORY ARRAY TESTER (8 CHANNEL) SETUP | | | | |
| Work Description | MEMORY ARRAY TESTER (8 CHANNEL) SETUP | | | | |
| Pre Qualification Details | Please refer Tender documents. | | | | |
| Tender Value in ₹ | | Product Category | Miscellaneous Goods | Sub category | NA |
| Contract Type | Tender | Bid Validity(Days) | 90 | Period Of Work(Days) | 14 |
| Location | Department of Electrical Engineering | Pincode | 600036 | Pre Bid Meeting Place | NA |
| Pre Bid Meeting Address | NA | Pre Bid Meeting Date | NA | Bid Opening Place | Department of Electrical Engineering |

Critical Dates

| | | | |
|--|----------------------|--|----------------------|
| Publish Date | 16-Feb-2021 04:00 PM | Bid Opening Date | 01-Mar-2021 04:00 PM |
| Document Download / Sale Start Date | 16-Feb-2021 04:10 PM | Document Download / Sale End Date | 01-Mar-2021 03:00 PM |
| Clarification Start Date | NA | Clarification End Date | NA |
| Bid Submission Start Date | 17-Feb-2021 09:00 AM | Bid Submission End Date | 01-Mar-2021 03:00 PM |

Tender Documents

| NIT Document | S.No | Document Name | Description | Document Size (in KB) |
|--------------|------|--------------------|----------------------------|-----------------------|
| | 1 | Tendernotice_1.pdf | As per the tender document | 435.78 |

| Work Item Documents | S.No | Document Type | Document Name | Description | Document Size (in KB) |
|---------------------|------|----------------------|---|--------------|-----------------------|
| | 1 | Additional Documents | ANNEXURE - V.pdf | Annexure - V | 143.03 |
| | 2 | Tender Documents | OPEN TENDER - MEMORY ARRAY TESTER (8 CHANNEL) SETUP.pdf | OPEN TENDER | 435.78 |

Tender Inviting Authority

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|----------------|---|
| Name | The Senior Manager |
| Address | The Senior Manager, Project Purchase, I.I.T. Madras, Sardar Patel Road, ICSR Building, 2nd floor, Chennai - 600 036 |

Tender Creator Details

| | |
|---------------------|----------------------|
| Created By | Vijay Shankar B |
| Designation | Assistant Registrar |
| Created Date | 16-Feb-2021 03:41 PM |