

**Published Corrigendum Details**

Date : 12-Jun-2018 05:52 PM

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| Organisation Chain : | Indian Institute of Technology Madras Deputy Registrar (Industrial Consultancy and Sponsored Research) |
| Tender ID : | 2018_IIT-M_324713_1 |
| Tender Ref No : | OED/NTCPWC/HPC/2018/02 |
| Tender Title : | Procurement of HPC Cluster 10 T flops and Graphics card for National Technology Centre for Ports Waterways and Coasts. |
| Corrigendum Type : | Date |

Corrigendum:1

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Critical Dates

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Details Before Corrigendum

| Critical Dates | | | |
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