



# Government ePublishing System

## Tender Details

Date : 23-Jan-2019 11:45 AM



### Basic Details

<b>Organisation Chain</b>	Indian Institute of Technology Madras  Deputy Registrar (Industrial Consultancy and Sponsored Research)		
<b>Tender Reference Number</b>	NTCPWC/FBGARTEC/KMUR/009		
<b>Tender ID</b>	2019_IIT-M_401819_1		
<b>Tender Type</b>	Limited.	<b>Form of contract</b>	Supply
<b>Tender Category</b>	Goods	<b>No. of Covers</b>	2
<b>Payment Mode</b>	Not Applicable	<b>Is Multi Currency Allowed For BOQ</b>	No
<b>Is Multi Currency Allowed For Fee</b>	No		

### Cover Details, No. Of Covers - 2

Cover No	Cover	Document Type	Description
1	Fee/PreQual/Technical	.pdf	Technical
2	Finance	.pdf	Financial

### Tender Fee Details, [Total Fee in ₹ \* - 0.00]

<b>Tender Fee in ₹</b>	0.00		
<b>Fee Payable To</b>	NA	<b>Fee Payable At</b>	NA
<b>Tender Fee Exemption Allowed</b>	NA		

### EMD Fee Details

<b>EMD Amount in ₹</b>	0.00	<b>EMD Exemption Allowed</b>	NA
<b>EMD Fee Type</b>	NA	<b>EMD Percentage</b>	NA
<b>EMD Payable To</b>	NA	<b>EMD Payable At</b>	NA

### Work / Item(s)

<b>Title</b>	FPGA Based Real Time Embedded Controller
<b>Work Description</b>	FPGA Based Real Time Embedded Controller - 2no.
<b>Pre Qualification</b>	Please refer Tender documents.

<b>Details</b>					
<b>Tender Value in ₹</b>		<b>Product Category</b>	Miscellaneous Goods	<b>Sub category</b>	NA
<b>Contract Type</b>	Tender	<b>Bid Validity(Days)</b>	90	<b>Period Of Work(Days)</b>	17
<b>Location</b>	NTCPWC, IIT Madras	<b>Pincode</b>	600036	<b>Pre Bid Meeting Place</b>	NA
<b>Pre Bid Meeting Address</b>	NA	<b>Pre Bid Meeting Date</b>	NA	<b>Bid Opening Place</b>	NTCPWC, New Academic Complex, IIT Madras

### Critical Dates

<b>Publish Date</b>	23-Jan-2019 11:50 AM	<b>Bid Opening Date</b>	08-Feb-2019 03:30 PM
<b>Document Download / Sale Start Date</b>	23-Jan-2019 11:55 AM	<b>Document Download / Sale End Date</b>	08-Feb-2019 03:00 PM
<b>Clarification Start Date</b>	NA	<b>Clarification End Date</b>	NA
<b>Bid Submission Start Date</b>	23-Jan-2019 01:00 PM	<b>Bid Submission End Date</b>	08-Feb-2019 03:00 PM

### Tender Documents

<b>NIT Document</b>	<b>S.No</b>	<b>Document Name</b>	<b>Description</b>	<b>Document Size (in KB)</b>
		1	Tendernotice_1.pdf	As per the tender document

<b>Work Item Documents</b>	<b>S.No</b>	<b>Document Type</b>	<b>Document Name</b>	<b>Description</b>	<b>Document Size (in KB)</b>
		1	Tender Documents	Tender Document - FPGA Based Real Time Embedded Controller.pdf	As per the tender document

### Tender Inviting Authority

<b>Name</b>	Dr. K. Murali
<b>Address</b>	The Project Co-ordinator, NTCPWC, Room no. 606, 6th Floor, New Academic Complex, IIT Madras Chennai 36

### Tender Creator Details

<b>Created By</b>	Sathyanarayanan V
<b>Designation</b>	Senior Manager, Project Purchase
<b>Created Date</b>	23-Jan-2019 11:41 AM