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19.06.2019

## Department of Electrical Engineering

### Corrigendum-1

**Tender Reference no:** ELE/RADA/013/2019  
**Name of the Item:** Custom 5GNR FPGA and RF boards  
**Corrigendum details:** Date extension

The submission date for above mentioned open tender has been extended to 20.06.2019 at 3 PM.

**All other terms and conditions remain the same.**

**Tender Inviting Authority:**

Senior Manager (Project Purchase)  
IC&SR, 2<sup>nd</sup> Floor, IIT Madras  
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